Appl. No.: 09/833,173
Amdt. dated: 10/4/2006
Reply to Office Action of April 4, 2006

This listing of claims will replace all prior versions, and listings, of claims in the application:

IN THE CLAIMS

1. (Currently Amended). A secure architecture for encoded or encrypted digital audio files comprising:

a computing platform for ~~receiving and~~ processing encrypted or encoded digital data, said computing platform including a host processor and a peripheral bus, said computing platform configured to run audio or video playback application software ~~and pass~~ for passing said encrypted or encoded digital data to said peripheral bus, said computing platform configured so that said peripheral bus is not-accessible by said audio or video playback software running on said computing platform;

a ~~peripheral~~ playback device for playing back encrypted or encoded digital data including a separate processor, a peripheral bus interface, a timing generator and a digital-to-analog converter (DAC) for receiving said encrypted or encoded digital signals from said peripheral bus and decrypting ~~and~~ or decoding said encrypted ~~and~~ or encoded data signals, said timing generator configured to generate timing signals for said DAC, said peripheral also including a memory device for storing decoding or decryption software, said peripheral interface coupled to said peripheral bus for receiving said encrypted and encoded digital signals from said peripheral bus, said peripheral configured to decrypt or decode said encrypted or encoded digital data and generate a decoded or decrypted analog output signal for playback by an external analog device.

2. (Previously Presented). The secure architecture as recited in claim 1, wherein said computing platform includes a network interface for receiving digital data from an external network.

3. (Previously Presented). The secure architecture as recited in claim 1, wherein said peripheral bus is a USB bus.

4. (Previously Presented). The secure architecture as recited in claim 1, wherein said peripheral bus is a PCI bus.

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5. (Previously Presented).   The secure architecture as recited in claim 1, wherein said peripheral bus is a Fire Wire bus.

6. (Previously Presented).   The secure architecture as recited in claim 1, further including one or more user input devices.

7. (Previously Presented).   The secure architecture as recited in claim 1, wherein said computing architecture includes one or more local persistent storage devices.

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